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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/537,509	11/30/2005	Fumitsugu Fukuyo	046124-5388	4531

23973 7590 05/25/2007  
DRINKER BIDDLE & REATH  
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ONE LOGAN SQUARE  
18TH AND CHERRY STREETS  
PHILADELPHIA, PA 19103-6996

EXAMINER
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GREENE, JAIRUS K

ART UNIT	PAPER NUMBER
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2891

MAIL DATE	DELIVERY MODE
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05/25/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## Office Action Summary

Application No.

10/537,509

Applicant(s)

FUKUYO ET AL.

Examiner

Jairus K. Greene

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 03 June 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-41 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☐ Claim(s) \_\_\_\_\_ is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☒ Claim(s) 1-41 are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 03 June 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date See Continuation Sheet.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

### *Election/Restrictions*

1. This application contains claims directed to the following five patentably distinct species: method of singulation wherein the substrate modified region is molten or a void, method of singulation wherein the fracture reaches the front and/or backside of the substrate, a method of singulation comprising grinding the rear face of the substrate, method of singulation wherein the laser light entrance surface is the rear face of the substrate, and a method of singulation wherein the singulated chip is *picked up* or removed from the singulation tape for mounting. The first and second aforementioned species are independent and distinct because, *stealth dicing* to generate a molten region or void versus a fracture within substrate encompass qualitatively and physically different laser and singulation application approach, technique, and results – particularly since applying mechanical (tensile) stress by expanding the tape will serve to heat the inner substrate as opposed to facilitating the *creation of cracks* (which is different than facilitating *crack propagation*). The first or second singulation method and the methods comprising grinding the rear surface of the substrate, using the rear surface of the substrate for laser exposure and picking the wafer up do not necessarily have to be in conjunction with each other to achieve the desired product. Hence they will be considered mutually exclusive species of the disclosed invention.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is

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finally held to be allowable. Currently, the generic claim of this invention reads as follows:

A method of cutting a semiconductor substrate, the method comprising the steps of: irradiating a semiconductor substrate having a sheet bonded thereto by way of a die-bonding resin layer with laser light while locating a light-converging point within the semiconductor substrate, so as to form a modified region caused by photon absorption within the semiconductor substrate, and causing the modified region to form a part which is intended to be cut; and expanding the sheet after the step of forming the part which is intended to be cut, so as to cut the semiconductor substrate and die-bonding resin layer along the part which is intended to be cut.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which depend from or otherwise require all the limitations of an allowable generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species.

MPEP § 809.02(a).

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jairus K. Greene whose telephone number is (571) 270-1747. The examiner can normally be reached on Monday thru Friday, 7:30am-5:00pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Bruce can be reached on (571) 272-2487. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



JKG



DAVID BRUCE  
SUPERVISOR, PATENT EXAMINER